# 10/C 5-24-24 Sulles

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE (RA-133, formerly RD-036)

In the Application of:

SIDIROPOULOS et al.

Serial No: 09/372,879

Filed: August 12, 1999

Title: Integrated Circuit Having I/O Structures with Reduced Input Loss

Assistant Commissioner for Patents

Group Art Unit: 2814

Examiner: TRAN, T. V.

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office, Fax No. (703)812-51/80m 5/22/02

Advisor Mullanu (name of person signing this certificate)

Assistant Commissioner for Patents

### RESPONSE TO THE OFICE ACTION DATED FEBRUARY 22, 2002

Dear Sir:

With reference to the Office Action dated February 22, 2002, kindly amend the abovereferenced application as follows:

#### IN THE CLAIMS:

Washington, DC 20231

Please <u>substitute</u> the following claims for the pending claims having the same claim number:

(A marked-up version showing insertions and deletions to the pending claims is attached as EXHIBIT A).

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MAY 2 2 2002

TECHNOLOGY CENTER 2800

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE (RA-133, formerly RD-036)

) Group Art Unit: 2814	
xaminer: TRAN, T. V.	
•	

Commissioner of Patents and Trademarks

Washington, D.C. 20231

#### TRANSMITTAL - RESPONSE TO OFFICE ACTION

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Dear Sir:

With respect to the above-identified application, transmitted herewith is a:

MAI Z Z LUUL

RESPONSE TO OFFICE ACTION (16 pages)

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### Fee Calculation:

(For the presentation of additional claims 29-37 in the Response to the Office Action enclosed herewith):

Surcharge for more than 20 total claims: ((9 additional claims) x \$18) \$ 162.00

Surcharge for more than 3 independent claims: ((0 additional claims) x \$84) \$ 0.00

Total Fees \$ 162.00

[XX] The Commissioner is hereby authorized to charge the fees, pursuant to 37 CFR §1.16, totaling § 162.00 for submittal of additional claims for consideration, to Deposit Account No. 50-0998. A duplicate copy of this sheet is enclosed.

[XX] The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. 50-0998.

Respectfully submitted,

Date: May 22, 2002

Rambus Inc. 4440 El Camino Real Los Altos, CA 94022 650-947-5336 Jose G. Moniz, Reg. No. 50,192

	IN THE UN	TED STATES PATENT (Case No. RA-133, fo	AND TRADEMAN	RK OFFICE	
In the Application of:			) Group Art	Unit: 2814	
SIDIROPOULOS et al.			) Examiner:	TRAN, T.V.	
Serial N	o: 09/372,879		)		
Filed:	August 12, 19	999	)		
Title:	Title: Integrated Circuit Having I/O Structures ) With Reduced Input Loss )				
Assistant Washing	Commissioner fo ton, DC 20231	FACSIMILE CO	OVER SHEET		
DATE:		May 22, 2002		FAX COPY RECEIVED	
TO:		Technology Cent	er 2800	MAY 2 2 2002	
	UMBER:	703-872-9318		TECHNOLOGY CENTER 2800	
NO OF PAGES:		19 including this c	over sheet		

### The attached:

NO. OF PAGES:

TRANSMITTAL - RESPONSE TO OFFICE ACTION (1 page in duplicate)

RESPONSE TO THE OFFICE ACTION DATED FEBRUARY 22, 2002 (16 pages)

OFFICIAL FILING

is being facsimile transmitted to the United States Patent and Trademark Office (Fax No.703-872-9318) in the above-referenced application.

Respectfully submitted,

Date: May 22, 2002

Rambus Inc. 4440 El Camino Real Los Altos, CA 94022

Adrienne Mullany Direct: 650-947-5412

Facsimile: 650-947-5001